

<b>Notice of References Cited</b>	Application/Control No. 10/511,575	Applicant(s)/Patent Under Reexamination PORST ET AL.	
	Examiner Keith Vicary	Art Unit 2183	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2003/0037221	02-2003	Gschwind et al.	712/3
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Optimized address assignment for DSPs with SIMD memory accesses," Lorenz et al. Proceedings of the 2001 conference on Asia South Pacific design automation, Pages: 415 - 420, January 30, 2001, cited as proof of inherency
	V	David Brooks, Margaret Martonosi, "Dynamically Exploiting Narrow Width Operands to Improve Processor Power and Performance," hpc, p. 13, The Fifth International Symposium on High Performance Computer Architecture, 1999
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.